

FIG. 1

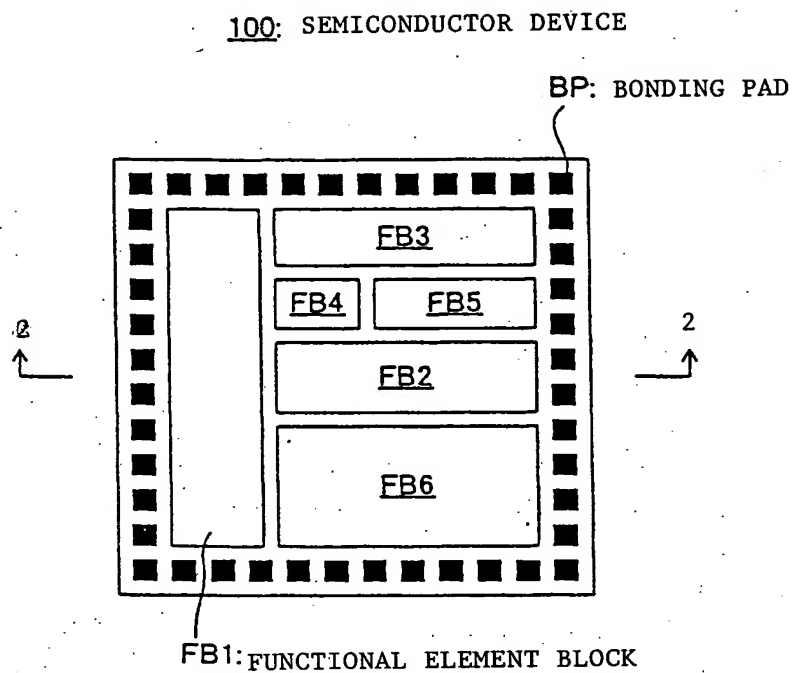


FIG. 2

100: SEMICONDUCTOR DEVICE

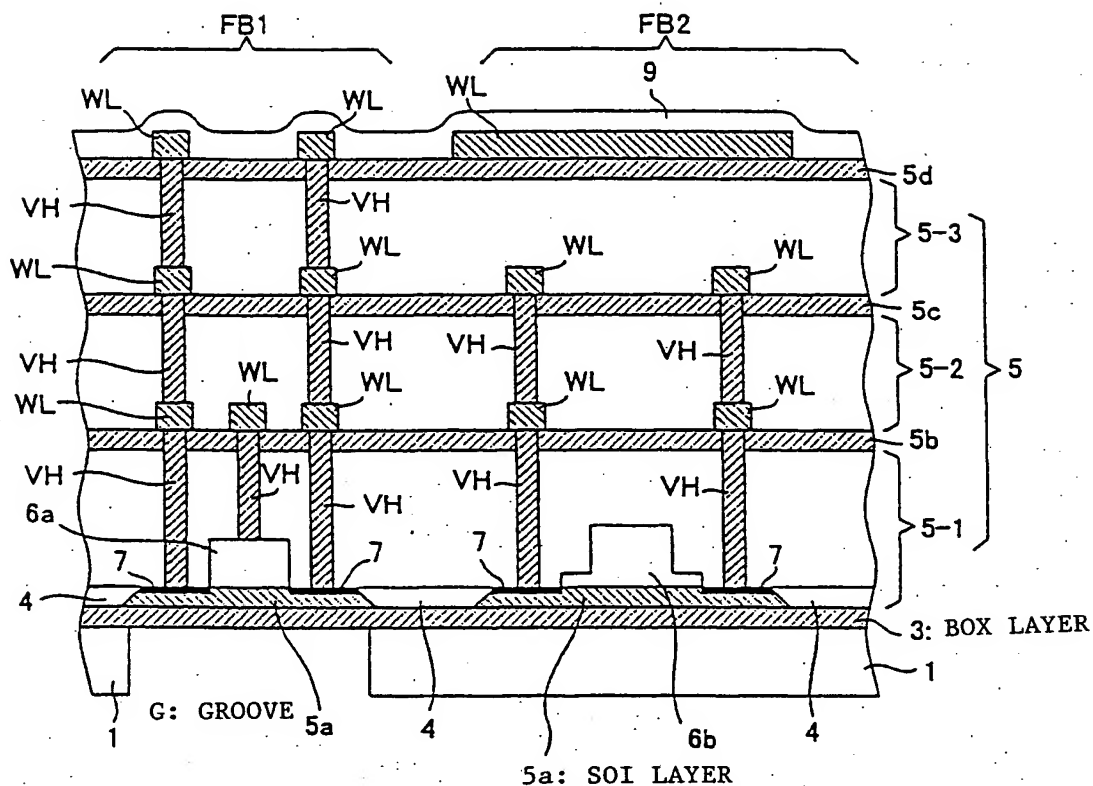


FIG. 3

100: SEMICONDUCTOR DEVICE

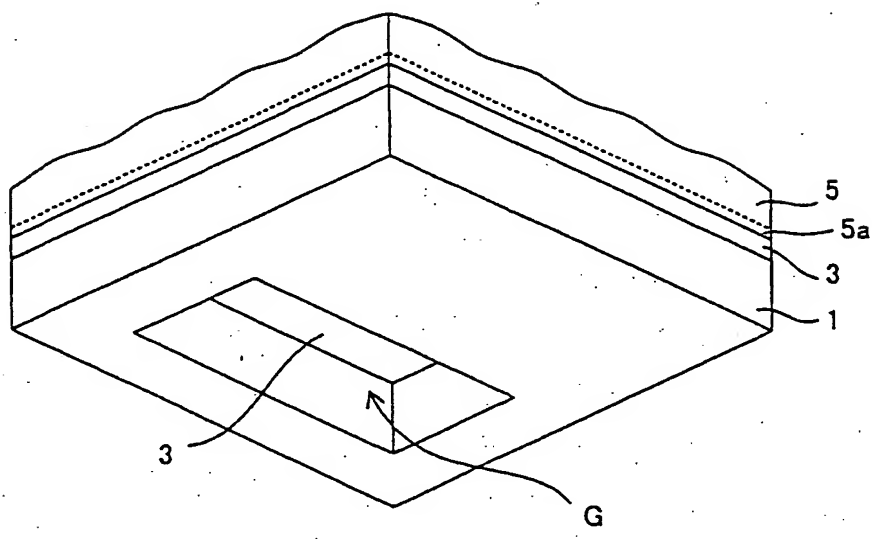


FIG. 4

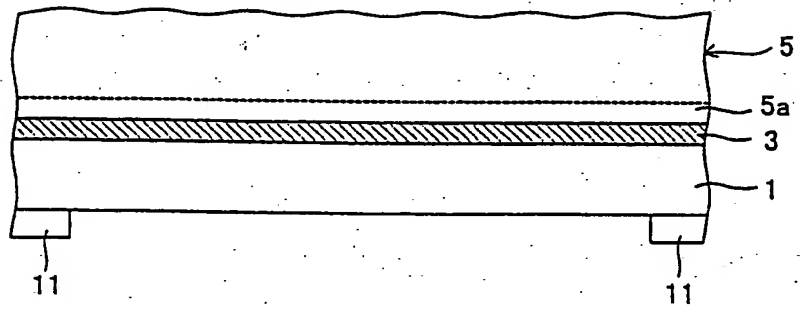


FIG. 5

100: SEMICONDUCTOR DEVICE

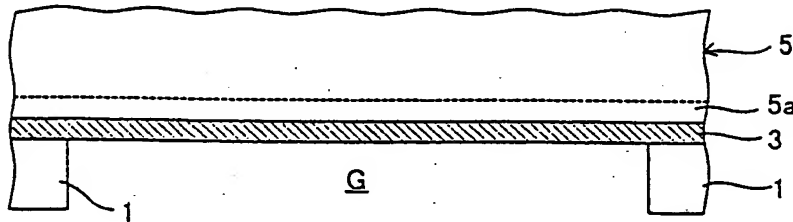
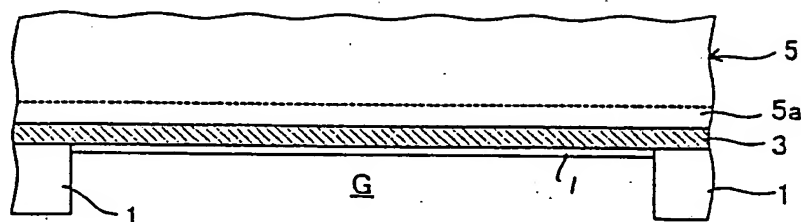


FIG. 5A

FIG. 6



200: SEMICONDUCTOR DEVICE

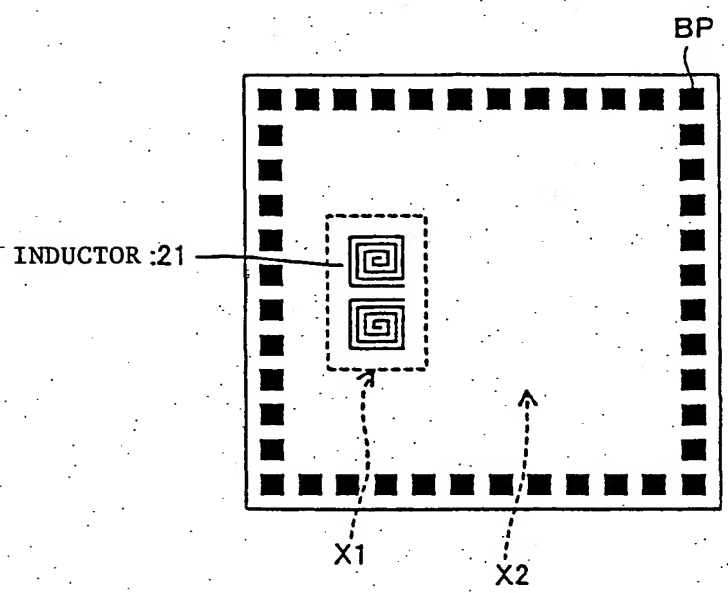


FIG. 7

200: SEMICONDUCTOR DEVICE

31: ETCHING MASK

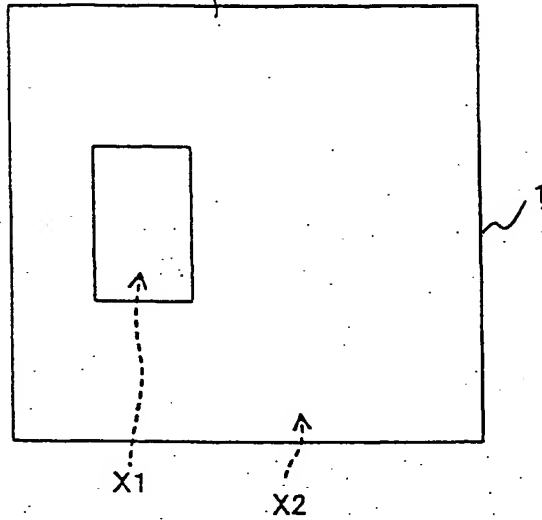


FIG. 8

300: SEMICONDUCTOR DEVICE

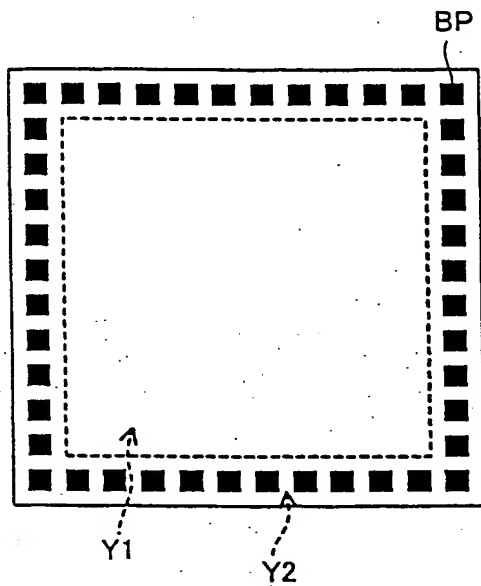


FIG. 9

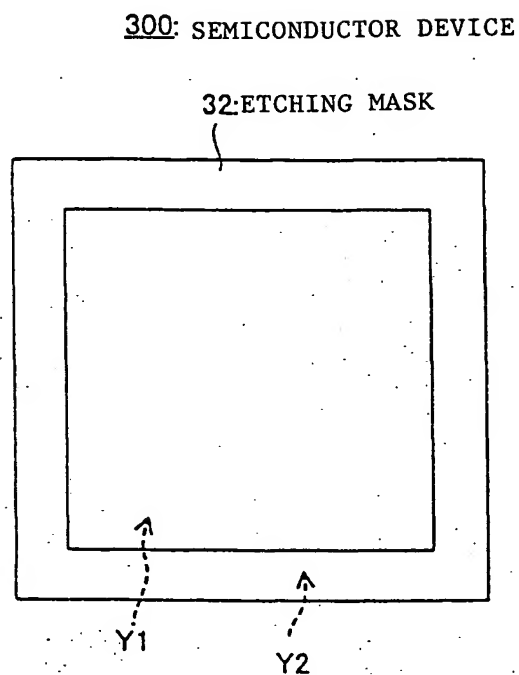


FIG. 10

